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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	New Divisional Application
				Filing Date	November 17, 2003
				First Named Inventor	Hiroyuki UMIMOTO et al.
				Art Unit	2825-2813
				Examiner Name	Belur V. Keshavan BLUM
Sheet	1	of	1	Attorney Docket Number	740819-1039

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Examiner Initials [*]	Cite No. ¹	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
DS		Shahidi et al. (October 1993) IEEE Electronics Device Letters, <i>High-Performance Devices for a 0.15µm CMOS Technology</i> , Vol. 14 No. 10 (pp.466-468).
↓		Bouillon et al. (1997) IEEE, <i>Anomalous short channel effects in Indium implemented nMOSFETs</i> , 4 pages.

Examiner Signature	DS	Date Considered	9/28/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.